

INDIAN INSTITUTE OF TECHNOLOGY DELHI  
Industrial Research & Development Unit

No.: IITD/IRD/MI00828/

5656

Dt.: /07/2013

**Advertisement No.: IITD/IRD/63/2013**

Applications are invited from Indian Citizens for adhoc appointment as per the details mentioned below for a maximum period of six months. The selection will be held through the DRC of Computer Science & Engineering Dept., IIT Delhi.

Title of the Project	Unified Model Component II: Model Implementation and Parallelization (MI00828)	
	Duration of the Project : upto 14/2/2015	
Post	Consolidated Pay Slab	Qualifications
Project Scientist	Rs.36,000/- per month (consolidated) or higher, based on experience.	1 <sup>st</sup> class B. Tech experience with parallel programming. System management experience preferred.
Date of Interview	Time of Interview	Venue
16/07/2013	03:30 pm	Committee Room Dept .of Computer Science & Engg. 4 <sup>th</sup> Floor, Bharti Building, IIT Delhi

The candidates who fulfill the above qualifications/experience should appear for interview. Kindly bring your formal application through proper channel along with Bio-data with complete information regarding educational qualifications indicating percentage of marks of each examination passed, details of work experience and a recent passport size photograph, along with photocopies certificates (both academic & professional) addressed to the **Assistant Registrar (IRD), IIT Delhi, Hauz Khas, New Delhi – 110016** on the date of interview. The advertisement No., Title of Project and Post applied for must be mentioned on top left side of the application. No candidate, who is already employed at the Institute /IRD shall be interviewed unless his/her application has not been duly forwarded by their concerned establishment sections. 5% relaxation of marks may be granted to the SC/ST Candidates.



(V.K. Vashistha)  
Assistant Registrar (IRD)

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