

INDIAN INSTITUTE OF TECHNOLOGY DELHI
Industrial Research & Development Unit

No.: IITD/IRD/RP02432/ 1784

Dt.: /02/2013

Advertisement No.: IITD/IRD/402/2012

Applications are invited from Indian Citizens for adhoc appointment as per the details mentioned below for a maximum period of six months. The selection will be held through the DRC of Computer Science & Engineering Dept., IIT Delhi.

Title of the Project	Algorithms & Complexity (RP02432)	
	Duration of the Project : upto 21/12/2015	
Funding Agency	DST	
Post	Consolidated Pay Slab	Qualifications
Sr. Research Fellow	Rs.18,000/- per month plus HRA @ 30% (as per rules)	First class M. Tech or equivalent in Computer Science.
Date of Interview	Time of Interview	Venue
05/03/2013	04:00 pm	Committee Room Dept .of Computer Science & Engg. 4 th Floor, Bharti Building, IIT Delhi

The candidates who fulfill the above qualifications/experience should appear for interview. Kindly bring your formal application through proper channel along with Bio-data with complete information regarding educational qualifications indicating percentage of marks of each examination passed, details of work experience and a recent passport size photograph, along with photocopies certificates (both academic & professional) addressed to the **Assistant Registrar (IRD), IIT Delhi, Hauz Khas, New Delhi – 110016** on the date of interview. The advertisement No., Title of Project and Post applied for must be mentioned on top left side of the application. No candidate, who is already employed at the Institute/IRD shall be interviewed unless his/her application has not been duly forwarded by their concerned establishment sections. 5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee his/her salary will be fixed as per prevailing IRD norms.


Assistant/Deputy Registrar

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- Prof. Naveen Garg (PI), CSE