

INDIAN INSTITUTE OF TECHNOLOGY DELHI
HAUZ KHAS, NEW DELHI – 110016
Industrial Research & Development Unit

No.: IITD/IRD/RP02670/

9187

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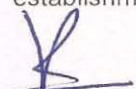
Dt.: /10/2012

Advertisement No.: IITD/IRD/297/2012

Applications are invited from Indian Citizens for adhoc appointment as per details mentioned below for a maximum period of six months.

Project Entitled	Development in Sensor Web Enablemet (RP02670)	
Name of the Funding Agency	DST	
Post	Consolidated Fellow ship	Qualifications
Research Associate	Rs.22,000-23,000-24,000/- + 30% HRA	Ph.D. in ECE / CSE plus experience in optical communication / FPGA Design/Embedded Systems/ E&C/ Telecom/ Physics with relevant Communication & Electronics background. OR 1 st class M. Tech. or equivalent in ECE /CSE plus 03 years experience post qualification in optical communication / FPGA Design/Embedded Systems/ E&C/ Telecom/ Physics with relevant Communication & Electronics background.
Jr. Research Fellow	Rs.16,000/- p.m. + 30% HRA	1 st class B.Tech./M.Sc. or equivale in Electronics / E&C/Telecom/ ET/Physics with relevant Communication and Electronics back ground. NET/GATE qualified
Date	Time	Venue
06/11/12	09:30 a.m. (Written Test) 03:30 p.m. (Interview)	Room No. 2A-105, Bharti School IIT Delhi IRD Conference Hall, 8 th Floor, Main Building, IIT Delhi

The candidates who fulfill the above qualifications / experience should appear for interview. Kindly bring your formal application through proper channel along with Bio-data with complete information regarding educational qualifications indicating percentage of marks of each examination passed, details of working experience and a recent passport size photograph, along with photocopies certificates (both academic & professional) addressed to the **Assistant Registrar (IRD), IIT Delhi, Hauz Khas, New Delhi – 110016** on the date of interview. The advertisement No., Title of Project and Post applied for must be mentioned on top left side of the application. No candidate, who is already employed at the Institute /IRD shall be interviewed unless his/her application has not been duly forwarded by their concerned establishment sections. 5% relaxation of marks may be granted to the SC/ST Candidates.


(V.K. Vashistha)
Asstt. Registrar, IRD

INDIAN INSTITUTE OF TECHNOLOGY DELHI
HAUZ KESHA, NEW DELHI - 110016
Faculty & Research & Development Unit

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Name of the Position	Qualification	Experience
Research Assistant	B.Tech. (ECE) M.Tech. (ECE) M.Phil. (ECE) Ph.D. (ECE)	Ph.D. in ECE / ESE plus experience in signal communication / FPGA Design/ VLSI System / ECG Telemetry Physics with relevant Communication & Electronics background. OR 1 st class B.Tech. or equivalent in ECE / ESE plus 2 nd class M.Tech. or equivalent in ECE / ESE plus 2 nd class B.Tech. or equivalent in signal communication / FPGA Design/ VLSI System / ECG Telemetry Physics with relevant Communication & Electronics background.
Research Fellow	B.Tech. (ECE) M.Tech. (ECE) M.Phil. (ECE) Ph.D. (ECE)	1 st class B.Tech. M.Sc. or equivalent in Electronics / ECE/Telemetry / ET/Physics with relevant Communication and Electronics background. NET/GATE qualified.
Address	Room No. 2A-106, Shree School IIT Delhi	Room No. 2A-106, Shree School IIT Delhi
Post	Post	Post
Salary	Salary	Salary
Age Limit	Age Limit	Age Limit
Application Form	Application Form	Application Form
Application Fee	Application Fee	Application Fee
Application Deadline	Application Deadline	Application Deadline
Interview Date	Interview Date	Interview Date
Interview Time	Interview Time	Interview Time
Interview Place	Interview Place	Interview Place
Selection Process	Selection Process	Selection Process
Final Offer	Final Offer	Final Offer

The candidates who have the above qualifications & experience should apply for this job by sending their forms to the Faculty & Research & Development Unit along with Bank draft of the amount mentioned regarding number of qualifications including percentage of marks in each qualification, copies of scoring certificate and a recent passport size photograph. They will also have to provide their bank account & professional address in the Application Form. The Application Form should be submitted to the Faculty & Research & Development Unit, IIT Delhi, Hauz Khas, New Delhi - 110016 on the date of invitation. The advertisement No. 1/2012 of the Institute should also be mentioned in top left corner of the application. No candidate who is already employed at the Institute should be considered unless their application has not been duly forwarded by their concerned department/center. The selection process will be decided by the Faculty & Research & Development Unit.

Dr. V. Venkatesh
Asstt. Registrar, IITD