Applications from Indian nationals are invited for Project Appointment under the following project. Appointment shall be on contractual basis with consolidated pay, renewable yearly or upto the duration of the project, whichever is earlier. The requirement of qualifying NET/GATE in the discipline OR M.Tech. 1st class with 6 years? Experience. Publications of 2 journals are required. Knowledge of Optical Communication / Networks / FPGA / Embedded systems is preferred.

**Title of the Project**
Turbulent resilient FSO systems (RP04568G)

<table>
<thead>
<tr>
<th>Post(s)</th>
<th>Consolidated fellowship / Pay-slab</th>
<th>Qualifications</th>
</tr>
</thead>
<tbody>
<tr>
<td>Principal Project Scientist (04)</td>
<td>Rs.56,000-60,000-64,000-69,000-74,000-79,000/- p.m. plus HRA @ 24%</td>
<td>M.Tech. or equivalent with &gt; 70% (or 7.0 CGPA on the scale of 10) in Electronics CGPA on the scale of 10 in Electronics and Communication Engineering or Electrical Engineering OR B.Tech. 75% (or 7.5 CGPA on a scale 10) in Electronics and Communication Engineering or Electrical Engineering with 3 years experience. The previous Optical Communication / Optical Networks / FPGA / USRP / MATLAB / MAC is preferred.</td>
</tr>
<tr>
<td>Sr. Project Scientist (04)</td>
<td>Rs.45,000-48,200-51,400-55,400-59,400-63,400/- p.m. plus HRA @ 24%</td>
<td>B.Tech, with GATE, or M.Sc. with &gt; 70% (or 7.0 CGPA on the scale of 10) in Electronics and Communication Engineering (ECE) or Electrical Engineering. The previous hands-on experience in the area of Optical Communication / Optical Networks / Circuit Designing / Lens Designing / FPGA / USRP / MAC is preferred.</td>
</tr>
<tr>
<td>Project Scientist (03)</td>
<td>Rs.35,400-37,900-40,400-43,600-46,800-50,000/- p.m. plus HRA @ 24%</td>
<td>Knowledge of Optical Components / PCB designing is preferred.</td>
</tr>
<tr>
<td>Sr. Project Assistant (Tech) (01)*</td>
<td>Rs.35,400-37,900-40,400-43,600-46,800-50,000/- p.m. plus HRA @ 24%</td>
<td>Knowledge of Optical Components / PCB designing is preferred.</td>
</tr>
</tbody>
</table>

The posts may be downgraded as per discretion of the Selection Committee if none of the candidate is found suitable for the post.

The candidates who are interested to apply for the above post should download Form No. IRD/REC-4 from the IRD Website (http://ird.iitd.ac.in/rec) of IIT Delhi and submit the duly filled form with complete information regarding educational qualifications indicating percentage of marks/division, details of work experience etc. by e-mail with advertisement No. on the subject line at email id: fouzdergourab@gmail.com and cc to Abhishek.dixit@ee.iitd.ac.in

IIT Delhi reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post and their name will be displayed on web link (http://ird.iitd.ac.in/shortlisted) along with the online interview details. Only short-listed candidates will be informed for online interview. In case any clarification is required on eligibility regarding the above post, the candidate may contact Prof. Abhishek Dixit at email id: abhishek.dixit@ee.iitd.ac.in

Contd...
5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms. अनुसूचित जाति / अनुसूचित जनजाति के उम्मीदवारों को अंकों की 5% छूट दी जा सकती है। एक सेवानिवृत्त सरकारी कर्मचारी के चयन के मामले में उसका वेतन वर्तमान आईआईटी मानदंड के अनुसार तय किया जाएगा। The last date for submitting the completed applications by email is 12/01/2024 by 5.00 p.m.

वितरण

- Head of the Deptt./Centres/Units :
- Webmaster, IRD
- Notice Boards
- Advertisement file
- Prof. Abhishek Dixit, Department of Electrical Engineering
- Copy to Chairperson, DRC/CRC

It is requested that the contents of the Above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit To put advertisement at IITD website.