

इंडियन इंस्टीट्यूट ऑफ टेक्नोलॉजी दिल्ली
हौज खास, नई दिल्ली -110016
(औद्योगिक अनुसंधान एवं विकास इकाई)
INDIAN INSTITUTE OF TECHNOLOGY DELHI
Hauz Khas, New Delhi-110016
(Industrial Research & Development Unit)

No. IITD/IRD/MI02055G/191328

Dated: 16/12/2019

Advertisement No.: IITD/IRD/245/2019

Candidates of Indian nationality are invited to appear for the **Walk-in test / interview** for project appointments under the following project. Appointment shall be on contractual basis on consolidated pay renewable yearly or upto the duration of the project, whichever is earlier. निम्नलिखित परियोजना के तहत परियोजना की नियुक्तियों के लिए भारतीय राष्ट्रीयता के उम्मीदवारों को वॉक-इन परीक्षा/साक्षात्कार के लिए उपस्थित होने के लिए आमंत्रित किया जाता है। अपॉइंटमेंट वार्षिक आधार पर या परियोजना की अवधि तक जो भी पहले हो समेकित वेतन पर संविदात्मक आधार पर होगा। The project involves design of instruments, displays, and control systems with a strong background in instrument design, sensors and interfacing, low-level / system level programming, database design and PCB design and manufacture. Enclosure design and testing, prototype fabrication using 3D / rapid prototyping techniques will be desirable.

Title of the Project	Development of Network Smart Silos for Rice and Wheat – A Proof of Concept (Under MFIRP Scheme – 2019 of IRD) (MI02055G)	
Funding Agency	Industrial Research & Development Unit, IIT Delhi	
Name of the Project Investigator	Prof. Subrat Kar	
Deptt./Centre	Centre for Sensors, Instrumentation and Cyber Physical Systems Engineering / EE	
Duration of the Project	Upto:23/09/2020	
Post (s)	Consolidated Pay-slab / Fellowship	Qualifications
Project Assistant (Tech.) (1)	Rs.25,000/- p.m. (consolidated)	Diploma (3 years duration) / Graduate Degree in appropriate discipline with 4 years relevant experience in relevant field OR ITI (2 years duration) in relevant trade with 8 years relevant work experience OR B.Tech. in Electronics or Instrumentation or Computer Engineering / Computer Science & Engineering with four years of working experience is desirable. Work experience in design and fabrication of embedded systems, control systems is required.
Walk In Test / Date of Interview	Time of Interview	Venue
07/01/2020	11.00 a.m.	Room No. 2A-105, Bharti School, Bharti Building, IIT Delhi, Hauz Khas, New Delhi-110016

The post may be downgraded as per discretion of the Selection Committee if none of the candidate is found suitable for the post. The candidates who fulfill the above qualifications/experience should appear for the interview. Kindly bring your formal application on Form No. IRD/REC-4, which can be downloaded from IRD Website (<http://ird.iitd.ac.in/rec>) through proper channel along with complete information regarding educational qualifications indicating percentage of marks of each examination passed, details of work experience and a recent passport size photograph, along with original certificates (both academic & professional) for verification on the date of interview. No candidate, who is already employed at the Institute / IRD shall be interviewed unless his/her application has been duly forwarded by their concerned establishment/sections. 5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms. अनुसूचित जाति / अनुसूचित जनजाति के उम्मीदवारों को अंकों की 5% छूट दी जा सकती है। एक सेवानिवृत्त सरकारी कर्मचारी के चयन के मामले में उसका वेतन वर्तमान आईआरडी मानदंडों के अनुसार तय किया जाएगा। In case any clarification is required on eligibility regarding the above post, the candidate **may contact Prof. Subrat Kar at email id: subrat@ee.iitd.ac.in**

सहायक कुल्सचिव, आईआरडी

वितरण

- Head of the Deptt./Centres/Units : It is requested that the contents of the Above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit
- Webmaster, IRD : To put advertisement at IITD website.
- Notice Boards
- Advertisement file
- Prof. Subrat Kar, PI, Centre for Sensors, Instrumentation and Cyber Physical Systems Engineering/EE
- Copy to Chairperson, DRC/CRC
- Dr. Harshita Bhatnagar, RD Coordinator, (R&D) Wing