

इंडियन इंस्टीट्यूट ऑफ टेक्नोलॉजी दिल्ली
 हाऊ खास, नई दिल्ली -110016
 (औद्योगिक अनुसंधान एवं विकास इकाई)
 INDIAN INSTITUTE OF TECHNOLOGY DELHI
 Hauz Khas, New Delhi-110016
 (Industrial Research & Development Unit)

No. IITD/IRD/RP04156G/

369164

Advertisement No.: IITD/IRD/192/2021

Dated: 10/11/2021

Applications from Indian nationals are invited for Project Appointment under the following project. Appointment shall be on contractual basis with consolidated pay, renewable yearly or upto the duration of the project, whichever is earlier. निम्नलिखित परियोजना के तहत भारतीय नागरिकों से आवेदन आमंत्रित किए जाते हैं। अपॉइंटमेंट, अनुबंधित आधार पर समेकित वेतन, नवीकरणीय वार्षिक या परियोजना की अवधि तक, जो भी पहले हो, के साथ होगा।

Title of the Project	Next Generation Wireless Research and Standardization on 5G and Beyond (RP04156G)	
Funding Agency	Ministry of Electronics and Information Technology	
Name of the Project Investigator	Prof. Brejesh Lall [email ID: 5g.bhartischool@gmail.com]	
Deptt./ Centre	Department of Electrical Engineering	
Duration of the Project	Upto: 29/07/2024	
Post (s)	Consolidated fellowship / Pay-slab	Qualifications
Research Associate-III (1)	Rs. 54,000/- p.m. plus HRA @ 24%	Ph.D/MD/MS/MDS or equivalent degree OR having 3 years of research, teaching and design and development experience after MVSc/M.Pharm/ME/M.Tech. with at least one research paper in Science Citation (SCI) Journal. Desirable: Experience with edge computing solutions Experience of working with standards documents Exposure to patent drafting process Hands-on with edge computing Knowledge of 5G system architecture.
Sr. Project Manager (1)	Rs. 56,000-Rs. 56,000-60,000-64,000-69,000-74,000-79,000/- p.m. plus HRA @ 24%	(i) A Post Graduate Degree with at least at 55% marks or its equivalent grade and (ii) 08 years of administrative experience in research establishments or other Institutions of higher education out of which 5 years as Assistant Registrar or equivalent post. Desirable: (i) An MBA or equivalent, (ii) Experience of computer system for information processing and retrieval. Desirable: Experience with edge computing solutions Experience of working with standards documents Exposure to patent drafting process Hands-on with edge computing Knowledge of 5G system architecture.
Research Associate-III (1)	Rs. 54,000/- p.m. plus HRA @ 24%	Ph.D/MD/MS/MDS or equivalent degree OR having 3 years of research, teaching and design and development experience after MVSc/M.Pharm/ME/M.Tech. with at least one research paper in Science Citation (SCI) Journal. Desirables: Experience with networking, 5G technologies and standards.

The candidates who are interested to apply for the above post should download Form No. IRD/REC-4 from the IRD Website (<http://ird.iitd.ac.in/rec>) of IIT Delhi and submit the duly filled form with complete information regarding educational qualifications indicating percentage of marks/division, details of work experience etc. by e-mail with advertisement No. on the subject line to Prof. Brejesh Lall at email id: 5g.bhartischool@gmail.com

IIT Delhi reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post and their name will be displayed on web link (<http://ird.iitd.ac.in/shortlisted>) alongwith the online interview details. Only short-listed candidates will be informed for online interview.

In case any clarification is required on eligibility regarding the above post, the candidate may contact Prof. Brejesh Lall at email id: 5g.bhartischool@gmail.com 5% relaxation of marks may be granted to the SC/ST Candidates. अनुसूचित जाति / अनुसूचित जनजाति के उम्मीदवारों को अंकों की 5% छूट दी जा सकती है The last date for submitting the completed applications by e-mail is 24/11/2021 by 5.00 p.m.

सहायक कुलसचिव, आईआरडी

वितरण

- Head of the Deptt./Centres/Units : It is requested that the contents of the Above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit
- Webmaster, IRD : To put advertisement at IITD website.
- Notice Boards
- Advertisement file
- Prof. Brejesh Lall, PI, Department of Electrical Engineering
- Copy to Chairperson, DRC/CRC
- Dr. Harshita Bhatnagar, RD Coordinator, (R&D) Wing