

INDIAN INSTITUTE OF TECHNOLOGY DELHI
Industrial Research & Development Unit
Hauz Khas, New Delhi -110016

No.: IITD/IRD/RP02825/

9148


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Dt.: /11/2013

Advertisement No.: IITD/IRD/181/2013

Applications are invited from Indian citizens for contractual appointment under the project. Appointments shall be on yearly contractual basis on consolidated pay slab.

Title of the Project	Compressive Sampling for Imaging Application (RP02825) Duration of Project: 04/11/2016	
Funding Agency	Akshi Technologies	
Post	Consolidated Fellowship	Qualifications
Research Associate	Rs.22,000/- p.m. plus HRA @ 20%	Essential Qualification: 1 ST class M. Tech./M.E./MSEE/B.E/B.Tech/BSEE in Electrical Engineering with minimum CGPA of 8/10 or 3.5/4 both at graduate and postgraduate level. Minimum 2 years experience in Analog Circuit Design and Knowledge of Sparsity. Desirable: Bio-signal (EEG Signal) detection and analysis and some exposure to compressive sampling.

The candidates who are interested to apply for the above post should send their applications with complete information regarding educational qualifications indicating percentage of marks/division, details of work experience and a recent passport size photograph along with an **application fee of Rs.100/- (non-refundable) in the form of a Bank Draft drawn preferably on State Bank of India in favor of Dean (IRD), IIT Delhi** addressed to the **Assistant Registrar, IRD Unit, Indian Institute of Technology (IIT) Delhi, Hauz Khas, New Delhi – 110016**. IIT Delhi reserves the right to fix suitable criteria for short-listing of eligible candidates satisfying advertised qualification and requirement of the project post. Only short-listed candidates will accordingly be informed. The last date for receipt of duly completed applications will be 15 days from the date of issue of this Advertisement. **The Advertisement No. & post applied for must be mentioned on the left side of the application.** 5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms.


(V.K. Vashistha)
Assistant Registrar (IRD)

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- Prof. S.D. Joshi, PI, Department of Electrical Engg.