

**INDIAN INSTITUTE OF TECHNOLOGY DELHI**  
**Industrial Research & Development Unit**  
**Hauz Khas, New Delhi -110016**

No.: IITD/IRD/RP02395/8909

Dt.: 01/11/2013

**Advertisement No.: IITD/IRD/172/2013**

Applications are invited from Indian Citizens for adhoc appointment as per details mentioned below for a maximum period of six months or up to the duration of the project, whichever is earlier.

Title of the Project	Non-Silicon Based Technologies for Nanofabrication and Nanoscale Devices (RP02395)	
	Duration of the Project : Upto 08/08/2015	
Funding Agency	DIT	
Post	Consolidated Fellowship	Qualifications
Project Scientist	Rs.32,000-51,200/- per month plus 20% HRA (as per rules)	1 <sup>st</sup> class M. Tech in Nanotechnology with 02 years of Research Experience.
Date of Interview	Time of Interview	Venue
22/11/2013	03:00 pm	Committee Room (Room No. 325) Centre for Atmospheric Sciences (CAS) II Floor, Block-VI, IIT Delhi

The candidates who fulfill the above qualifications/experience should appear for the interview. Kindly bring your formal application through proper channel along with Bio-data with complete information regarding educational qualifications indicating percentage of marks of each examination passed, details of work experience and a recent passport size photograph, along with photocopies certificates (both academic & professional) addressed to the **Assistant Registrar (IRD), IIT Delhi, Hauz Khas, New Delhi – 110016** on the date of interview. The advertisement No., Title of Project and Post applied for must be mentioned on top left side of the application. No candidate, who is already employed at the Institute /IRD shall be interviewed unless his/her application has not been duly forwarded by their concerned establishment sections. 5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms.

  
(V.K. Vashistha)  
Assistant Registrar (IRD)

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- Prof. M. Jagadesh Kumar (PI), Dept. of Elect. Engg./NRF