Applications from Indian nationals are invited for Project Appointment under the following project. Appointment shall be on contractual basis with consolidated pay, renewable yearly or upto the duration of the project, whichever is earlier.

**Title of the Project**: A Data-Centric Approach to Study Fundamental Limits of Communication in Adversarial Wireless Networks (RP04066G)

**Funding Agency**: Ministry of Electronics and Information Technology

**Name of the Project Investigator**: Prof. Harshan Jagadeesh

[Email ID: jharshan@ee.iitd.ac.in]

**Deptt/ Centre**: Department of Electrical Engineering

**Duration of the Project**: Upto: 16/02/2023

<table>
<thead>
<tr>
<th>Post(s)</th>
<th>Consolidated fellowship / Pay-slab</th>
<th>Qualifications</th>
</tr>
</thead>
<tbody>
<tr>
<td>Project Assistant (Tech.) (2)</td>
<td>Rs.29,200-31,200-33,200-35,800-38,400-41,000/- p.m. plus HRA @ 24%</td>
<td>1st class B.Tech. in the field of Electronics and Communication or Computer Science, with 2 years experience in the relevant field. The candidates must have strong background in signal processing and communications, and must be proficient in programming. Prior experience in handling software defined radios, Raspberry Pis and other wireless boards is an advantage.</td>
</tr>
</tbody>
</table>

The candidates who are interested to apply for the above post should download Form No. IRD/REC-4 from the IRD Website (http://ird.iitd.ac.in/rec) of IIT Delhi and submit the duly filled form with complete information regarding educational qualifications indicating percentage of marks/division, details of work experience etc. by e-mail with advertisement No. on the subject line to Prof. Harshan Jagadeesh at email id: jharshan@ee.iitd.ac.in

IIT Delhi reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post and their name will be displayed on web link (http://ird.iitd.ac.in/shortlisted) along with the online interview details. Only short-listed candidates will be informed for online interview.

In case any clarification is required on eligibility regarding the above post, the candidate may contact Prof. Harshan Jagadeesh at email id: jharshan@ee.iitd.ac.in

5% relaxation of marks may be granted to the SC/ST Candidates. The last date for submitting the completed applications by e-mail is 09/09/2021 by 5.00 p.m.

**Advertisement No.:** IITD/IRD/151/2021

Dated: 26/8/2021

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**Notice**

- Head of the Deptt./Centres/Units: It is requested that the contents of the Above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit
- Webmaster, IRD: To put advertisement at IITD website.
- Notice Boards
- Advertisement file
- Prof. Harshan Jagadeesh, PI, Department of Electrical Engineering
- Copy to Chairperson, DRC/CRC
- Dr. Harshita Bhatnagar, RD Coordinator, (R&D) Wing

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सहायक कुलसचिव, आईआरडी