

इंडियन इंस्टीट्यूट ऑफ टेक्नोलॉजी दिल्ली
हौज खास, नई दिल्ली -110016
(औद्योगिक अनुसंधान एवं विकास इकाई)
INDIAN INSTITUTE OF TECHNOLOGY DELHI
Hauz Khas, New Delhi-110016
(Industrial Research & Development Unit)

No. IITD/IRD/MI01776G/253412

Dated: 28/08/2020

Advertisement No.: IITD/IRD/134/2020

Applications from Indian nationals are invited for Project Appointment under the following project. Appointment shall be on contractual basis with consolidated pay, renewable yearly or upto the duration of the project, whichever is earlier. निम्नलिखित परियोजना के तहत भारतीय नागरिकों से आवेदन आमंत्रित किए जाते हैं। अपॉइंटमेंट, अनुबंधित आधार पर समेकित वेतन, नवीकरणीय वार्षिक या परियोजना की अवधि तक, जो भी पहले हो, के साथ होगा।

Title of the Project	LiFi network (Sub Project # 1 under RP03521) (MI01776G)	
Funding Agency	Department of Telecommunications	
Name of the Project Investigator	Prof. Abhishek Dixit	
Deptt./Centre	Department of Electrical Engineering	
Duration of the Project	Upto:31/03/2021	
Post (s)	Consolidated Pay-slab / Fellowship	Qualifications
Sr. Project Scientist (3)	Rs.45,000-48,200-51,400-55,400-59,400-63,400/- p.m. plus HRA @ 24%	M. Tech. or equivalent with > 70% (or 7.0 CGPA on the scale of 10) in Electronics and Communication Engineering or Electrical Engineering with 3 years experience. The previous hands-on experience in the area of optical communication / optical Network FPGA / USRP / MAC is preferred.
Project Scientist (2)	Rs.35,400-37,900-40,400-43,600-46,800-50,000/- p.m. plus HRA @ 24%	B.Tech (with GATE* qualified), 1st class or equivalent. The previous hands-on experience in the area of optical communication/PCB designing/FPGA / PSoC / USRP / MATLAB / MAC is preferred. *The requirement of qualifying GATE qualification may be relaxed by the Committee in case of highly meritorious candidates.

The post may be downgraded as per discretion of the Selection Committee if none of the candidate is found suitable for the post.

The candidates who are interested to apply for the above post should download **Form No. IRD/REC-4** from the IRD Website (<http://ird.iitd.ac.in/rec>) of IIT Delhi and submit the duly filled form with complete information regarding educational qualifications indicating percentage of marks/division, details of work experience etc. **by e-mail with advertisement No. on the subject line to Prof. Abhishek Dixit at email id: gaurav.pandey@ee.iitd.ac.in; or rajsharmaraj91@gmail.com**

IIT Delhi reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post and their name will be displayed on web link (<http://ird.iitd.ac.in/shortlisted>) alongwith the interview details. Only short-listed candidates will be informed. In case any clarification is required on eligibility regarding the above post, the candidate may contact **Prof. Abhishek Dixit at email id: abhishek.dixit@iitd.ac.in**

5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms. अनुसूचित जाति / अनुसूचित जनजाति के उम्मीदवारों को अंकों की 5% छूट दी जा सकती है। एक सेवानिवृत्त सरकारी कर्मचारी के चयन के मामले में उसका वेतन वर्तमान आईआरडी मानदंडों के अनुसार तय किया जाएगा। **The last date for submitting the completed applications by e-mail is 11/09/2020 by 5.00 p.m.** The shortlisted candidates who are called for interview should bring original certificates (both professional and academic) with a recent passport size photograph at the time of interview.


सहायक कुलसचिव, आईआरडी

वितरण

- Head of the Deptt./Centres/Units : It is requested that the contents of the Above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit
- Webmaster, IRD : To put advertisement at IITD website.
- Notice Boards
- Advertisement file
- Prof. Abhishek Dixit, PI, Department of Electrical Engineering
- Copy to Chairperson, DRC/CRC
- Dr. Harshita Bhatnagar, RD Coordinator, (R&D) Wing