Applications from Indian nationals are invited for Project Appointment under the following project. Appointment shall be on contractual basis with consolidated pay, renewable yearly or up to the duration of the project, whichever is earlier.

<table>
<thead>
<tr>
<th>Post(s)</th>
<th>Consolidated fellowship / Pay-slab</th>
<th>Qualifications</th>
</tr>
</thead>
<tbody>
<tr>
<td>Research Associate (1)</td>
<td>Rs.67,000/-p.m. (consolidated including HRA @ 24%)</td>
<td>Essential Qualifications: Ph.D. or equivalent degree or having 3 years of research, teaching and design &amp; development experience after ME/M.Tech./M.S.(R). Basic skills in Firmware, OS/Kernal, Device Drivers etc on Unix, Microsoft, IOS, Android platforms, Cloud Platforms like AWS/Azure/IBM/Google/Intel based. Expertise in machine learning algorithms and platforms such as Tensorflow/Pytorch and or Embedded/IOT technology development. Should have Good communication and scientific writing skills.</td>
</tr>
<tr>
<td>Sr. Research Fellow (1)</td>
<td>Rs.35,000/-p.m. plus HRA @ 24%</td>
<td>First class with not less than 60% aggregate (or) not less than 6.5 CGPA on 10-point scale in B.E/B.Tech and M.E / M. Tech in EE/EC/CS/IT/Instrumentation/Bio-Medical with CSIR NET/GATE* qualified and 2 years of experience in relevant field. Expertise in C#, Unity, machine learning algorithms and platforms such as Tensorflow/Pytorch. Should have Good communication and scientific writing skills.</td>
</tr>
<tr>
<td>Sr. Research Fellow (1)</td>
<td>Rs.35,000/-p.m. plus HRA @ 24%</td>
<td>First class with not less than 60% aggregate (or) not less than 6.5 CGPA on 10-point scale in B.E/B.Tech and M.E / M. Tech in EE/EC/CS/IT/Instrumentation/Bio-Medical with CSIR NET/GATE* qualified and 2 years of experience in relevant field. Expertise in Electronics Instrumentations/Circuit Development/Embedded Technology/ Hardware-software interfacing domain/ IoT applications and knowledge of coding. Should have Good communication and scientific writing skills.</td>
</tr>
</tbody>
</table>

*The requirement of NET/GATE examination for the selection to the post of JRF/SRF may be relaxed for the candidates who have graduated from Centrally Funded Technical Institutes (CFTIs) with a CGPA of more than 8.000 (80% aggregate marks).
The candidates who are interested to apply for the above post should download Form No. IRD/REC-4 from the IRD Website (http://ird.iitd.ac.in/rec) of IIT Delhi and submit the duly filled form with complete information regarding educational qualifications indicating percentage of marks/division, details of work experience etc. by email to Prof. Tapan Kumar Gandhi at email id: bstiitdelhi@gmail.com

IIT Delhi reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post and their name will be displayed on web link (http://ird.iitd.ac.in/shortlisted) along with the online interview details. Only short-listed candidates will be informed for online interview. In case any clarification is required on eligibility regarding the above post, the candidate may contact Prof. Tapan Kumar Gandhi at email id: tgandhi@iitd.ac.in

5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms. The last date for submitting the completed applications by email is 01/07/2021 by 5.00 p.m. The shortlisted candidates who are called for interview should bring original certificates (both professional and academic) with a recent passport size photograph at the time of interview.

निर्देश
- Head of the Deptt./Centres/Units : It is requested that the contents of the Above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit
- Webmaster, IRD : To put advertisement at IITD website.
- Notice Boards
- Advertisement file
- Prof. Tapan Kumar Gandhi, PI, Department of Electrical Engineering
- Copy to Chairperson, DRC/CRC
- Dr.Harshita Bhatnagar, RD Coordinator, (R&D) Wing