Applications from Indian nationals are invited for Project Appointment under the following project. Appointment shall be on contractual basis with consolidated pay, renewable yearly or upto the duration of the project, whichever is earlier. The qualifications and requirement of the post are as follows:

**Title of the Project:** Next Generation Wireless Research and Standardization on 5G and Beyond (RP04156G)

**Funding Agency:** Ministry of Electronics and Information Technology

**Name of the Project Investigator:** Prof. Brejesh Lall

**Dept/ Centre:** Department of Electrical Engineering

**Duration of the Project:** Upto: 29/07/2024

<table>
<thead>
<tr>
<th>Post (s)</th>
<th>Consolidated fellowship / Pay-slab</th>
<th>Qualifications</th>
</tr>
</thead>
<tbody>
<tr>
<td>Project Scientist (2)</td>
<td>Rs. 35,400-37,900-40,400-43,600-46,800-50,000/- p.m. plus HRA @ 24%</td>
<td>B. Tech. / M.Tech (with GATE qualification) 1st class or equivalent in the appropriate discipline. Must have knowledge of wireless communication networks along with Good knowledge of coding theory, detection and estimation theory, cooperative communications, MIMO, 5G standards and testbed, low latency communications, machine learning/deep learning and Programming proficiency in C/C++/MATLAB and working knowledge of C/EWIT’s Broadband Wireless Simulator. <em>The requirement of qualifying GATE/ qualification may be relaxed by the Committee in case of highly meritorious candidates.</em></td>
</tr>
<tr>
<td>Jr. Research Fellow (1)</td>
<td>Rs. 31,000/- p.m. plus HRA @ 24%</td>
<td>Graduate / Post Graduate Degree in Engineering (ECE/CSE/IT or equivalent) with GATE along with Good knowledge of coding theory, detection and estimation theory, cooperative communications, MIMO, 5G standards and testbed, low latency communications, machine learning/deep learning and Programming proficiency in C/C++/MATLAB and working knowledge of C/EWIT’s Broadband Wireless Simulator. <em>The requirement of qualifying NET/GATE examination for the selection to the post of JRF/SRF may be relaxed for the candidates who have graduated from Centrally Funded Technical Institutes (CFTIs) with a CGPA of more than 8.000 (80% aggregate marks).</em></td>
</tr>
</tbody>
</table>

The candidates who are interested to apply for the above post should download Form No. IRD/REC-4 from the IRD Website (http://ird.iitd.ac.in/rec) of IIT Delhi and submit the duly filled form with complete information regarding educational qualifications indicating percentage of marks/division, details of work experience etc. by e-mail with advertisement No. on the subject line to Prof. Brejesh Lall at email id: 5gbhartischool@gmail.com

**NOTE:** Those who have already applied under the same project for same posts need not apply again.

IIT Delhi reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post and their name will be displayed on web link (http://ird.iitd.ac.in/shortlisted) along with the online interview details. Only short-listed candidates will be informed for online interview. In case any clarification is required on eligibility regarding the above post, the candidate may contact Prof. Brejesh Lall at email id: 5gbhartischool@gmail.com 5% relaxation of marks may be granted to the SC/ST Candidates. *The last date for submitting the completed applications by e-mail is 20/07/2022 by 5.00 p.m.*

**Head of the Deptt./Centres/Units :**

**Webmaster, IRD :**

**Notice Boards :**

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**Prof. Brejesh Lall, PI, Department of Electrical Engineering :**

**Copy to Chairperson, DRC/CRC :**

**Dr. Harshita Bhatnagar, RD Coordinator, (R&D) Wing :**

It is requested that the contents of the Above Advt. be brought to the notice of the staff working in your Dept./Centre/Unit To put advertisement at IITD website.