Applications from Indian nationals are invited for Project Appointment under the following project. Appointment shall be on contractual basis with consolidated pay subject to periodic performance review, and renewable yearly or up to the duration of the project, whichever is earlier. Indian nationals are invited to apply for the post of Project Assistant (Admin) (01).

**Title of the Project:** Secure Networks and Edge-Computing Hardware for Industry 4.0 (RP04579G)

**Funding Agency:** Ministry of Electronics and Information Technology

**Name of the Project Investigator:** Prof. Harshan Jagadeesh (PI) / Prof. Smruti Ranjan Sarangi (Co-PI)

**Deptt/Centre:** Department of Electrical Engineering

**Duration of the Project:** Upto: 16/10/2025

<table>
<thead>
<tr>
<th>Post(s)</th>
<th>Consolidated fellowship / Pay-slab</th>
<th>Qualifications</th>
</tr>
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<tbody>
<tr>
<td>Project Assistant</td>
<td>Rs.29,200-31,200-33,200-35,800-38,400-41,000/- p.m. plus HRA @ 24%</td>
<td>(1) PG degree with 55% marks (2) One-year diploma in computer applications (Office, etc.) (3) 5 years experience in admin. work in central govt or autonomous organizations like IIT Delhi (4) 5 years experience in doing graphics-related work (Adobe Photoshop and Illustrator) (5) Proof of training in procurement processes. (6) Experience in supporting remote courses like NPTEL</td>
</tr>
</tbody>
</table>

The candidates who are interested to apply for the above post should download Form No. IRD/REC-4 from the IRD Website (http://ird.iitd.ac.in/rec) of IIT Delhi and submit the duly filled form with complete information regarding educational qualifications indicating percentage of marks/division, details of work experience etc. by e-mail with advertisement No. on the subject line to Prof. Harshan Jagadeesh at email id:srsarangi@cse.iitd.ac.in

IIT Delhi reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post and their name will be displayed on web link (http://ird.iitd.ac.in/shortlisted) along with the online interview details. Only short-listed candidates will be informed for online interview. In case any clarification is required on eligibility regarding the above post, the candidate may contact to Prof. Harshan Jagadeesh at email id:srsarangi@cse.iitd.ac.in

5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms. A postgraduate qualification in Electrical Engineering will be an added advantage. The last date for submitting the completed applications by e-mail is 31/01/2024 by 5.00 p.m.

**Advertisement No.: ITD/IRD/022/2024**

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**Head of the Deptt./Centres/Units:** It is requested that the contents of the Above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit

**Webmaster, IRD**

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**Advertisement file**

**Prof. Harshan Jagadeesh, PI, Department of Electrical Engineering**

**Prof. Smruti Ranjan Sarangi, Co-PI, Department of Computer Science & Engineering**

**Copy to Chairperson, DRC/CRC**

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**Dated: 17/01/2024**

**Indian Institute of Technology Delhi**

**Hauz Khas, New Delhi-110016**

(Industrial Research & Development Unit)