Corrigendum

Advertisement No.: IITD/IRD/139/2023


This refers to the advertisement released for the post of Research Associate-III under the sponsored research project entitled "Next Generation Wireless Research and Standardization on 5G and Beyond" (RP04156G) in operation under Prof. Brejesh Lall, Department of Electrical Engineering.

The revised qualifications for the post of Research Associate-III may be read as under instead of notified earlier:

Ph.D in ECE or CSE or ELEC Engineering and having 3 years of research, design, network / edge slicing and development experience, with at least one research paper in Science Citation (SCI) Journal. Desirable: Experience of working with standards documents Exposure to patent drafting process and Knowledge of 5G system architecture, slicing, Edge computing. Telecom industry relevant experience will be preferred.

The other contents of the earlier released advertisement shall remain same.

This issues with the approval of the Competent Authority.

Assistant Registrar, IRD

Distribution

1. Head of the Deptt./Centres/Units
   It is requested that the corrigendum in respect of above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit

2. Notice Boards

3. Advertisement file

4. Prof. Brejesh Lall, PI, Department of Electrical Engineering

5. Webmaster IRD
   To put corrigendum at IITD website.
Applications from Indian nationals are invited for Project Appointment under the following project. Appointment shall be on contractual basis with consolidated pay, renewable yearly or upto the duration of the project, whichever is earlier. The candidates who are interested to apply for the above post should download Form No. IRD/REC-4 from the IRD Website (http://ird.iitd.ac.in/rec) of IIT Delhi and submit the duly filled form with complete information regarding educational qualifications indicating percentage of marks/division, details of work experience etc. by e-mail with advertisement No. on the subject line to Prof. Brejesh Lall at email id:5g.bhartischool@gmail.com

NOTE: Those who have already applied under the same project for same posts need not apply again.
IIT Delhi reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post and their name will be displayed on web link (http://ird.iitd.ac.in/shortlisted) along with the online interview details. Only short-listed candidates will be informed for online interview. In case any clarification is required on eligibility regarding the above post, the candidate may contact Prof. Brejesh Lall at email id: 5g.bhartischool@gmail.com 5% relaxation of marks may be granted to the SC/ST Candidates.

The last date for submitting the completed applications by e-mail is 31/07/2023 by 5.00 p.m.

It is requested that the contents of the Above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit.

To put advertisement at IITD website.

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- Head of the Deptt./Centres/Units
- Webmaster, IRD
- Notice Boards
- Advertisement file
- Prof. Brejesh Lall, PI, Department of Electrical Engineering
- Copy to Chairperson, DRC/CRC