

इंडियन इंस्टीट्यूट ऑफ टेक्नोलॉजी दिल्ली
हौज खास, नई दिल्ली -110016
(औद्योगिक अनुसंधान एवं विकास इकाई)
INDIAN INSTITUTE OF TECHNOLOGY DELHI
Hauz Khas, New Delhi-110016
(Industrial Research & Development Unit)

No. IITD/IRD/RP05049G/

554945

Dated:15/10/2026

Advertisement No.: IITD/IRD/294/2026

Candidates of Indian nationality are invited to appear for the **Walk-in test / interview** for project appointments under the following project. Appointment shall be on contractual basis on consolidated pay renewable yearly or upto the duration of the project, whichever is earlier. निम्नलिखित परियोजना के तहत परियोजना की नियुक्तियों के लिए भारतीय राष्ट्रियता के उम्मीदवारों को वॉक-इन परीक्षा/साक्षात्कार के लिए उपस्थित होने के लिए आमंत्रित किया जाता है। अपॉइंटमेंट वार्षिक आधार पर या परियोजना की अवधि तक समेकित वेतन पर संविदात्मक आधार पर जो भी पहले हो, होगा.

Title of the Project	TDOA-based RF emitter localization. (RP05049G)	
Funding Agency	Directorate of Futuristic Technology Management (DFTM)	
Name of the Project Investigator	Prof. Seshan Srirangarajan, Department of Electrical Engineering (email ID: seshan@ee.iitd.ac.in)	
Deptt./ Centre	Department of Electrical Engineering	
Duration of the Project	Upto: 22.06.2028	
Post (s)	Consolidated Pay-slab / Fellowship	Qualifications
Project Scientist (01)	Rs. 47,000 - 48,410 - 49,860 - 51,360 - 52,900 - 54,490 - 56,120 - 57,800 - 59,530 - 61,320/-p.m. plus HRA @30%	1 st class BTech / MSc / Post Graduate in Electronics Engineering or related discipline with first class or equivalent. Essential skills: 1. Proficiency in programming and working with Raspberry Pi, microcontrollers, and embedded Linux platforms such as STM32, ESP32, Arduino, etc. 2. Hands-on experience with Software Defined Radios (SDRs), RF signal acquisition, and interfacing SDRs with embedded computing platforms. 3. Strong programming skills in Python and C/C++, including embedded firmware and real-time data acquisition. 4. Ability to independently develop, integrate, test, debug, and validate hardware and software systems. 5. Experience in hardware integration, PCB/system-level interfacing, testing, and troubleshooting of electronic and embedded systems. Desirable skills: 1. Experience with time synchronization and timing systems, including synchronization of distributed nodes using GPS/GNSS, PPS, PTP, NTP, or other precision timing techniques. 2. Experience with TDOA/TOA-based localization, direction finding, or distributed RF sensing systems. 3. Familiarity with SDR platforms and RF hardware, including USRP, RTL-SDR, HackRF, ADALM-Pluto, or similar platforms. 4. Familiarity with real-time data acquisition, high-speed data transfer, networking, and synchronization of multiple sensor nodes. 5. Experience in signal processing, data visualization, and scientific/technical reporting.
Walk In Test / Date of Interview	Time of walk-in-Interview	Venue
30/10/2026	02.45 p.m. onwards	Room No. 105, Bharti School, IIT Delhi, Hauz Khas, New Delhi - 110 016.

The candidates who fulfill the above qualifications/experience should appear for the interview. Kindly bring your formal application on Form No. IRD/REC-4, which can be downloaded from IRD Website (<http://ird.iitd.ac.in/rec>) through proper channel along with complete information regarding educational qualifications indicating percentage of marks of each examination passed, details of work experience and a recent passport size photograph, along with original certificates (both academic & professional) for verification on the date of interview. No candidate, who is already employed at the Institute / IRD shall be interviewed unless his/her application has been duly forwarded by their concerned establishment/sections. 5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms. अनुसूचित जाति / अनुसूचित जनजाति के उम्मीदवारों को अंकों की 5% छूट दी जा सकती है. एक सेवानिवृत्त सरकारी कर्मचारी के चयन के मामले में उसका वेतन वर्तमान आईआरडी मानदंडों के अनुसार तय किया जाएगा। In case any clarification is required on eligibility regarding the above post, the candidate may contact at e-mail id: seshan@ee.iitd.ac.in

वितरण

- Head of the Deptt./Centres/Units :
- Webmaster, IRD :
- Notice Boards
- Advertisement file
- Prof. Seshan Srirangarajan, PI, Deptt. of Elect. Engg.
- Copy to Chairperson, DRC/CRC

It is requested that the contents of the above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit To put advertisement at IITD website.

Deputy Registrar (IRD Estt.)

15/10/26