

इंडियन इंस्टीट्यूट ऑफ टेक्नोलॉजी दिल्ली
हौज खास, नई दिल्ली -110016
(औद्योगिक अनुसंधान एवं विकास इकाई)
INDIAN INSTITUTE OF TECHNOLOGY DELHI
Hauz Khas, New Delhi-110016
(Industrial Research & Development Unit)

No. IITD/IRD/RM00210G_SN/ 554131

Dated:13/10/2026

Advertisement No.: IITD/IRD/279/2026

Applications from Indian nationals are invited for project appointment under the following project. Appointment shall be on contractual basis with consolidated pay, renewable yearly or upto the duration of the project, whichever is earlier. निम्नलिखित परियोजना के तहत भारतीय नागरिकों से आवेदन आमंत्रित किए जाते हैं। अपॉइंटमेंट, अनुबंधित आधार पर समेकित वेतन, नवीकरणीय वार्षिक या परियोजना की अवधि तक, जो भी पहले हो, के साथ होगा।

Title of the Project	Finding the capacity of multiterminal classical and quantum information theoretic tasks under realistic constraints. (RM00210G SN)	
Funding Agency	Anusandhan National Research Foundation, Science and Engineering Research Board	
Name of the Project Investigator	Prof. Aditya Nema [email: adityanema@ee.iitd.ac.in]	
Deptt./Centre	Department of Electrical Engineering	
Duration of the Project	Upto:07.07.2029	
Post (s)	Consolidated fellowship / Pay-slab	Qualifications
Jr. Research Fellow) – (01)	Rs. 37,000/-p.m. Plus HRA @27%	B.E./B.Tech/B.Sc.+M.Sc. with 75% or 7.5 CGPA or above and GATE qualified in EE,CS, Maths or Physics without M.Tech. M.Tech with CGPA 7.5 or above. Expertise in one of quantum information theory, quantum error correction or classical information theory is strongly desired. *The requirement of NET/GATE examination for the selection to the post of JRF/SRF may be relaxed for the candidates who have graduated from Centrally Funded Technical Institutes (CFTIs) with a CGPA of more than 8.000 (80% aggregate marks).

The candidates who are interested to apply for the above post should download **Form No. IRD/REC-4** from the IRD Website (<http://ird.iitd.ac.in/rec>) of IIT Delhi and submit the duly filled form with complete information regarding educational qualifications indicating percentage of marks/division, details of work experience etc. **by e-mail with advertisement No. on the subject line to Prof. Aditya Nema at email id: adityanema@ee.iitd.ac.in**

IIT Delhi reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post and their name will be displayed on web link (<http://ird.iitd.ac.in/shortlisted>) alongwith the online interview details. Only short-listed candidates will be informed for online interview. In case any clarification is required on eligibility regarding the above post, the candidate may contact **Prof. Aditya Nema at email id: adityanema@ee.iitd.ac.in**

5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms. अनुसूचित जाति / अनुसूचित जनजाति के उम्मीदवारों को अंकों की 5% छूट दी जा सकती है। एक सेवानिवृत्त सरकारी कर्मचारी के चयन के मामले में उसका वेतन वर्तमान आईआरडी मानदंडों के अनुसार तय किया जाएगा। **The last date for submitting the completed applications by email is 26/10/2026 by 5.00 p.m.**


Deputy Registrar, IRD Estt.

वितरण

- Head of the Deptt./Centres/Units :
- Webmaster, IRD :
- Notice Boards :
- Advertisement file :
- Prof. Aditya Nema, PI, Department of Electrical Engineering
- Copy to Chairperson, DRC/CRC

It is requested that the contents of the Above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit

To put advertisement at IITD website.