

No. IITD/IRD/RM00164N/ 545266

Dated:08/09/2026

**Advertisement No.: IITD/IRD/240/2026**

Applications from Indian nationals are invited for project appointment under the following project. Appointment shall be on contractual basis with consolidated pay, renewable yearly or upto the duration of the project, whichever is earlier. निम्नलिखित परियोजना के तहत भारतीय नागरिकों से आवेदन आमंत्रित किए जाते हैं। अपॉइंटमेंट, अनुबंधित आधार पर समेकित वेतन, नवीकरणीय वार्षिक या परियोजना की अवधि तक, जो भी पहले हो, के साथ होगा।

| Title of the Project               | Modelling and Exploration of Communication Strategies in Chiplet-based Systems-on-Chip. (RM00164N)               |                                                                                                                                                                                           |
|------------------------------------|------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Funding Agency                     | BAYA SEMICONDUCTOR INDIA PRIVATE LIMITED                                                                         |                                                                                                                                                                                           |
| Name of the Project Investigator   | Prof. Kolin Paul/Prof. Preeti Ranjan Panda<br>[email ID: panda@cse.iitd.ac.in]                                   |                                                                                                                                                                                           |
| Deptt/ Centre                      | Department of Computer Science & Engineering                                                                     |                                                                                                                                                                                           |
| Duration of the Project            | Upto: 03.06.2029                                                                                                 |                                                                                                                                                                                           |
| Post (s)                           | Consolidated fellowship / Pay-slab                                                                               | Qualifications                                                                                                                                                                            |
| Principal Project Scientist - (01) | Rs. 64,260 - 66,190 - 68,180 - 70,220 - 72,330 - 74,500 - 76,730 - 79,030 - 81,400 - 83,840/- p.m. plus HRA @27% | PhD or (1 <sup>st</sup> class MTech/MSR with 6 years experience) in CS/IT/EE/related areas. Strong background in hardware, computer architecture, CPU simulators.                         |
| Sr. Project Scientist - (01)       | Rs. 60,750 - 62,570 - 64,450 - 66,380 - 68,370 - 70,420 - 72,530 - 74,710 - 76,950 - 79,260/-p.m. plus HRA       | 1 <sup>st</sup> class MTech/MSR or (1 <sup>st</sup> class BTech with 3 years experience) in CS/IT/EE/related areas. Strong background in hardware, computer architecture, CPU simulators. |
| Project Scientist - (01)           | Rs. 47,790 - 49,220 - 50,700 - 52,220 - 53,790 - 55,400 - 57,060 - 58,770 - 60,530 - 62,350/-p.m. plus HRA @27%  | 1 <sup>st</sup> class BTech in CS/IT/EE/related areas. Strong background in hardware, computer architecture, CPU simulators.                                                              |

The posts may be downgraded as per discretion of the Selection Committee if none of the candidate is found suitable for the post.

The candidates who are interested to apply for the above post should download **Form No. IRD/REC-4** from the IRD Website (<http://ird.iitd.ac.in/rec>) of IIT Delhi and submit the duly filled form with complete information regarding educational qualifications indicating percentage of marks/division, details of work experience etc. **by e-mail with advertisement No. on the subject line to Prof. Preeti Ranjan Panda at email id: panda@cse.iitd.ac.in**

IIT Delhi reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post and their name will be displayed on web link (<http://ird.iitd.ac.in/shortlisted>) alongwith the online interview details. Only short-listed candidates will be informed for online interview. In case any clarification is required on eligibility regarding the above post, the candidate may contact at email id: panda@cse.iitd.ac.in

5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms. अनुसूचित जाति / अनुसूचित जनजाति के उम्मीदवारों को अंकों की 5% छूट दी जा सकती है। एक सेवानिवृत्त सरकारी कर्मचारी के बचन के मामले में उसका वेतन वर्तमान आईआरडी मानदंडों के अनुसार तय किया जाएगा। **The last date for submitting the completed applications by email is 21/09/2026 by 5.00 p.m.**

  
Deputy Registrar (IRD Estt.)

**वितरण**

- Head of the Deptt./Centres/Units :
- Webmaster, IRD
- Notice Boards
- Advertisement file
- Prof. Kolin Paul, PI, Department of Computer Science & Engineering
- Copy to Chairperson, DRC/CRC

It is requested that the contents of the Above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit  
To put advertisement at IITD website.