

INDIAN INSTITUTE OF TECHNOLOGY
Hauz Khas, New Delhi – 110016
Industrial Research & Development Unit

No.: IITD/IRD/MI00233/

6604

8.
Dt: /08/2012

Advertisement No.: IITD/IRD/209/2012

Applications are invited from Indian citizens for purely temporary appointment under the research project. Appointments shall be on yearly contractual basis on consolidated pay slabs.

1.	Project Entitled	:	"Bharti School of Telecommunication Technology & Management, IIT Delhi (MI00233)
2.	Posts & Consolidated Pay slabs	:	A. Project Associate – Rs.27,500-44,000/- plus 20 % HRA (as per norms) B. Project Assistant - Rs. 23,500-37,600/- plus 20 % HRA (as per norms) (For Telecom Software Lab experience with Unix Administration) C. Project Assistant - Rs. 23,500-37,600/- plus 20 % HRA (as per norms) (For Data Interworking Lab experience with Embedded Systems)
3.	Qualification for Post A	:	1 st class B.Tech.(CS/ECE/IT/allied streams) with experience in Unix administration/ RHCE/CCNA. OR 1 st class M.Tech. plus relevant experience.
	Qualification for Post B & C-	:	1 st class B.Tech.(CS/ECE/IT/allied streams) with experience in Unix administration/ RHCE/CCNA/ Embedded Systems/ VHDL. OR Diploma (03 years duration Graduate Degree in (CS/ECE/IT Allied streams) with 04 years relevant work experience in relevant field.

The candidates who are interested to apply for the above post should send their applications with complete information regarding educational qualifications indicating percentage of marks/division, details of working experience and a recent passport size photograph along with an **Application Fee (non-refundable) in the form of Bank Draft drawn preferably on State Bank of India in favor of Dean(IRD), IIT Delhi of Rs.100/- for the post of Project Associate and Rs.50/- for the post of Project Assistant** addressed to the Prof. Subrat Kar, Deptt. of Electrical Engg., Room No. 2A-105, Bharati School, Indian Institute of Technology Delhi, Hauz Khas, New Delhi – 110016. IIT Delhi reserves the right to fix suitable criteria for short-listing of eligible candidates satisfying advertised qualification and requirement of the project post. Only short-listed candidates will accordingly be informed. The last date for receipt of duly completed applications will be 15 days from the date of issue of this Advertisement. **The Advertisement No. & post applied for must be mentioned on the left side of the application.** 5% relaxation of marks may be granted to the SC/ST Candidates.


(V.K. Vashistha)
Asstt. Registrar, IRD

Distribution:

- Head of the Deptt./Centres/Units → It is requested that the contents of the above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit
- Notice Boards
- Advertisement file
- Prof./Dr. Subrat Kar, PI, Electrical Engg.
- IRD Webmaster → To put advertisement at IITD website.