

INDIAN INSTITUTE OF TECHNOLOGY DELHI
Industrial Research & Development Unit
Hauz Khas, New Delhi -110016
Tel.:011-26597534

No. IITD/IRD/MI00233/102496

Dated: 01/11/2017

Advertisement No.: IITD/IRD/198/2017

Candidates of Indian Nationality are invited to appear for the **Walk-in Test/ Interview** for project appointments under the following project. Appointment shall be on contractual basis on consolidated pay renewable yearly or upto the duration of the project, whichever is earlier.

Title of the Project	Bharti school of Telecom Technology & Mgt, IIT Delhi (MI00233)	
Funding Agency	Indian Institute of Technology Delhi	
Name of the Project Investigator	Prof. Brejesh Lall	
Dept./Centre	Department of Electrical Engineering / BSTTM	
Duration of the project	31/03/2021	
Post(s)	Consolidated Fellowship	Qualifications
Project Assistant (Tech.) (1)	Rs.23,500-25,850-28,000-30,550-32,900-35,250-37,600/- per month plus HRA @ 20%	Diploma (03 years duration) in ECE (Electronics and Communication Engg.) with 04 years relevant work experience in electronics hardware components and design. Desirable Qualification: Good communication skills and working knowledge of electronics hardware components, latest development boards and their interfacing.
Date of Interview	Time of Interview	Venue
17/11/2017	09:00 am	Room No.-2A-101, Bharti Building, Indian Institute of Technology Delhi, Hauz Khas, New Delhi-110016

The candidates who fulfill the above qualifications/experience should appear for the interview. Kindly bring your formal application on Form No. IRD/REC-4, which can be downloaded from IRD Website (<http://ird.iitd.ac.in/rec>) through proper channel along with complete information regarding educational qualifications indicating percentage of marks of each examination passed, details of work experience and a recent passport size photograph, along with original certificates (both academic & professional) for verification on the date of interview. No candidate, who is already employed at the institute / IRD shall be interviewed unless his/her application has been duly forwarded by their concerned establishment/ sections. 5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/ superannuated government employee, his/her salary will be fixed as per prevailing IRD Norms.

In case any clarification is required on eligibility regarding the above post, the candidate **may contact Prof. Brejesh Lall at e-mail id: bharti.coord@admin.iitd.ac.in**

(V.K. Vashistha)
Assistant Registrar (IRD)

Distribution:

- Head of the Deptt./Centres/Units : It is requested that the contents of the above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit.
- Webmaster, IRD : To put advertisement at IITD website.
- Notice Boards
- Advertisement file
- Prof. Brejesh Lall (PI), Dept. of Electrical Engineering / BSTTM
- Copy to Chairperson, DRC/CRC