

इंडियन इंस्टीट्यूट ऑफ टेक्नोलॉजी दिल्ली
हौज खास, नई दिल्ली -110016
(औद्योगिक अनुसंधान एवं विकास इकाई)
INDIAN INSTITUTE OF TECHNOLOGY DELHI
Hauz Khas, New Delhi-110016
(Industrial Research & Development Unit)

No. IITD/IRD/RP03751G/132850

Dated:15/07/2019

Advertisement No.: IITD/IRD/129/2019

Candidates of Indian nationality are invited to appear for the **Walk-in test / interview** for project appointments under the following project. Appointment shall be on contractual basis on consolidated pay renewable yearly or upto the duration of the project, whichever is earlier. निम्नलिखित परियोजना के तहत परियोजना की नियुक्तियों के लिए भारतीय राष्ट्रियता के उम्मीदवारों को वॉक-इन परीक्षा/साक्षात्कार के लिए उपस्थित होने के लिए आमंत्रित किया जाता है। अपॉइंटमेंट वार्षिक आधार पर या परियोजना की अवधि तक समेकित वेतन पर संविदात्मक आधार पर जो भी पहले हो, होगा.

Title of the Project	Modelling & Control of Electromagnetic Levitation Systems (RP03751G)	
Funding Agency	Science and Engineering Research Board (SERB), DST	
Name of the Project Investigator	Prof. Mashuq-Un-Nabi	
Deptt./Centre	Department of Electrical Engineering	
Duration of the Project	Upto:22/05/2022	
Post (s)	Consolidated Pay-slab	Qualifications
Jr. Research Fellow (1)	Rs.31,000/-p.m. plus HRA @ 24%	EQ: Masters in Electrical / Electronic Engineering with Specialization in Control System or Power Electronics & Machine Drives. CGPA of 8 or above in Masters. Strongly desirable: 1) Exposure to power electronics based hardware circuit design, 2) Familiarity of Dspace Controller & associate hardware. Exposure to basic control related concepts. Fuzzy Logic etc. Throughout good academic record with NET/GATE qualification. "The candidate must satisfy the qualifications and eligibility conditions of JRF as notified by DST, Ministry of Science and Technology, (GoI), vide OM No. SR/S9/Z08/2018 dated January 30, 2019. The requirement of NET/GATE examination for the selection to the post of JRF/SRF may be relaxed for the candidates who have graduated from Centrally Funded Technical Institutes (CFTIs) with a CGPA of more than 8.000 (80% aggregate marks).
Walk In Test / Date of Interview	Time of Interview	Venue
02/08/2019	03.00 p.m.	Room No. 339, Block-II, (CODES-Lab), Department of Electrical Engineering, IIT DELHI, Hauz Khas, New Delhi-110016.

The candidates who fulfill the above qualifications/experience should appear for the interview. Kindly bring your formal application on Form No. IRD/REC-4, which can be downloaded from IRD Website (<http://ird.iitd.ac.in/rec>) through proper channel along with complete information regarding educational qualifications indicating percentage of marks of each examination passed, details of work experience and a recent passport size photograph, along with original certificates (both academic & professional) for verification on the date of interview. No candidate, who is already employed at the Institute / IRD shall be interviewed unless his/her application has been duly forwarded by their concerned establishment/sections. 5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms. अनुसूचित जाति / अनुसूचित जनजाति के उम्मीदवारों को अंकों की 5% छूट दी जा सकती है. एक सेवानिवृत्त सरकारी कर्मचारी के चयन के मामले में उसका वेतन वर्तमान आईआरडी मानदंडों के अनुसार तय किया जाएगा।

In case any clarification is required on eligibility regarding the above post, the candidate **may contact Prof. Mashuq-Un-Nabi at email id: mnabi@ee.iitd.ac.in**

(रमा शर्मा)

सहायक कुलसचिव, आईआरडी

वितरण

- Head of the Deptt./Centres/Units : It is requested that the contents of the Above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit
- Webmaster, IRD : To put advertisement at IITD website.
- Notice Boards
- Advertisement file
- Prof. Mashuq-Un-Nabi, PI, Department of Electrical Engineering
- Copy to Chairperson, DRC/CRC