

INDIAN INSTITUTE OF TECHNOLOGY DELHI
Industrial Research & Development Unit
Hauz Khas, New Delhi -110016
Tel.:011-26597534

No. IITD/IRD/RP02903/

Date: 15/07/2017

Advertisement No.: IITD/IRD/119/2017

Candidates of Indian nationality are invited to appear for the **Walk-in test / interview** for project appointments under the following project. Appointment shall be on contractual basis on consolidated pay rene/ wable yearly or upto the duration of the project, whichever is earlier.

Title of the Project	Investigation of Finite-time, Optimal and Robust Non-Singular Control Techniques with Application to Non-linear Systems (RP02903)	
Funding Agency	Science and Engineering Research Board (SERB)	
Name of the Project Investigator	Prof. S. Janardhanan	
Deptt./Centre	Department of Electrical Engineering	
Duration of the Project	Upto: 10/12/2017	
Post (s)	Consolidated Pay-slab	Qualifications
Jr. Research Fellow (1)	Rs.25,000/- p.m. plus HRA @ 30%	1 st class M.Tech. or M.S. in Engineering with relevant work experience of at least one year in Control Engineering related area. Work done as a part of Post Graduate Degree will not be considered as experience.
Walk In Test / Date of Interview	Time of Interview	Venue
01/08/2017	11.00 a.m.	Room No.-214, Control Lab, Block-II, Department of Electrical Engineering, Indian Institute of Technology Delhi, Hauz Khas, New Delhi-110016 Ph:011-26591091

The candidates who fulfill the above qualifications/experience should appear for the interview. Kindly bring your formal application on Form No. IRD/REC-4, which can be downloaded from IRD Website (<http://ird.iitd.ac.in/rec>) through proper channel along with complete information regarding educational qualifications indicating percentage of marks of each examination passed, details of work experience and a recent passport size photograph, along with original certificates (both academic & professional) for verification on the date of interview. No candidate, who is already employed at the Institute / IRD shall be interviewed unless his/her application has been duly forwarded by their concerned establishment/sections. 5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms.

In case any clarification is required on eligibility regarding the above post, the candidate **may contact Prof. S. Janardhanan at his e-mail id: janas@ee.iitd.ac.in**

(V.K. Vashistha)
A.R., IRD

Distribution:

- Head of the Deptt./Centres/Units : It is requested that the contents of the above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit
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- Notice Boards
- Advertisement file
- Prof. S. Janardhanan, PI, Department of Electrical Engg.
- Copy to Chairperson, DRC/CRC