

**INDIAN INSTITUTE OF TECHNOLOGY DELHI**  
**Industrial Research & Development Unit**  
**Hauz Khas, new Delhi-110016**

Tel.: 011-26597534

No. IITD/IRD/RP03320/33671

Date: 03/04/2017

**Advertisement No.: IITD/IRD064/2017**

Candidates of Indian Nationality are invited to appear for the Walk-in Test/Interview for project appointments under the following project. Appointment shall be on contractual basis on consolidated pay renewable yearly or upto the duration of the project, whichever is earlier. The candidates who fulfill the qualifications/ experience given below should email their applications (pdf) to sunita.cseiidt@gmail.com on or before 15<sup>th</sup> April, 2017 to get priority for interview. These candidates would be interviewed first on 24/04/2017. A test may also be conducted for certain posts.

Title of the Project(s)	On Board Phase 2: Design, Implementation and Testing of Improved Version Based on Feedback from the Pilot Trial (RP03320)	
Funding Agencies	Department of Science & Technology	
Name of the Project Investigator	Prof. M. Balakrishnan	
Deptt/Centre	Department of Computer Science & Engineering	
Duration of the Project	Upto: 30/11/2017	
Post(s)	Consolidated Pay-Slab	Qualifications
Project Scientist (1) Or Project Associate (Software Design Specialization)	Rs. 32,000 – 35,200-38,400- 41,600-44,800-48,000-51,200/- p.m. plus 20% HRA  Or  Rs. 27,500- 30,250-33,000- 35,750-38,500-41,250-44,000/- p.m. plus HRA @20%	<b>Essential Qualifications:</b> 1 <sup>st</sup> class B.Tech (with 2 years of experience) /M.Tech in Electronics or Computer Science or related field. Embedded software development experience. Interfacing with sensors with communication modules like RF/Bluetooth/Ethernet and related protocols. Experience with development tools like keil/AVR studio etc. <b>Desirable Qualifications:</b> Experience in developing Arduino/Raspberry-Pi & other processor based embedded systems
Project Assistant (Tech) (1)	Rs. 23,500 – 25,850-28,000- 30,550-32,900-35,250-37,600/- p.m. plus HRA @20%	<b>Essential Qualifications:</b> 1st Class B.Tech in Computer Science/IT/Electronics with four years experience. <b>Desirable Qualifications:</b> Experience in implementation, quality assurance (including testing), delivery and maintenance of Software. Strong embedded C/C++/Python programming skills. Documenting the project related information like flow diagrams, requirement specifications, test schedule, wireless communication protocol in firmware programming based on micro controller.
<b>Walk In Test/Date of Interview</b>	<b>Time</b>	<b>Venue</b>
24/04/2017	09:00 am for Project Assistant 10.00 a.m. for Project Associate	<b>Committee Room No-113,, SIT Building, Indian Institute of Technology Delhi, Hauz Khas, New Delhi-110016 Ph: 011-26591285</b>

The post may be downgraded as per discretion of the Selection Committee if none of the candidate is found suitable for the post.

The candidates who fulfill the above qualifications/experience should appear for the interview. Kindly bring your formal application on Form No. IRD/REC-4, which can be downloaded from IRD Website (<http://ird.iitd.ac.in/rec>) through proper channel along with complete information regarding educational qualifications indicating percentage of marks of each examination passed, details of work experience and a recent passport size photograph, along with original certificates (both academic & professional) for verification on the date of interview. No candidate, who is already employed at the Institute / IRD shall be interviewed unless his/her application has been duly forwarded by their concerned establishment/sections. 5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms.

In case any clarification is required on eligibility regarding the above post, the candidate may contact Ms. Sunita at her e-mail id: [sunita.cseiitd@gmail.com](mailto:sunita.cseiitd@gmail.com)

**(V.K. Vashistha)**  
**A.R., IRD**

#### **Distribution**

- Head of the Deptt./Centres/Units : It is requested that the contents of the Above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit
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- Notice Boards
- Advertisement file
- Prof. M. Balakrishnan, PI, Department of CS&Engg.
- Copy to Chairperson, DRC/CRC